7. BLOCK DIAGRAM

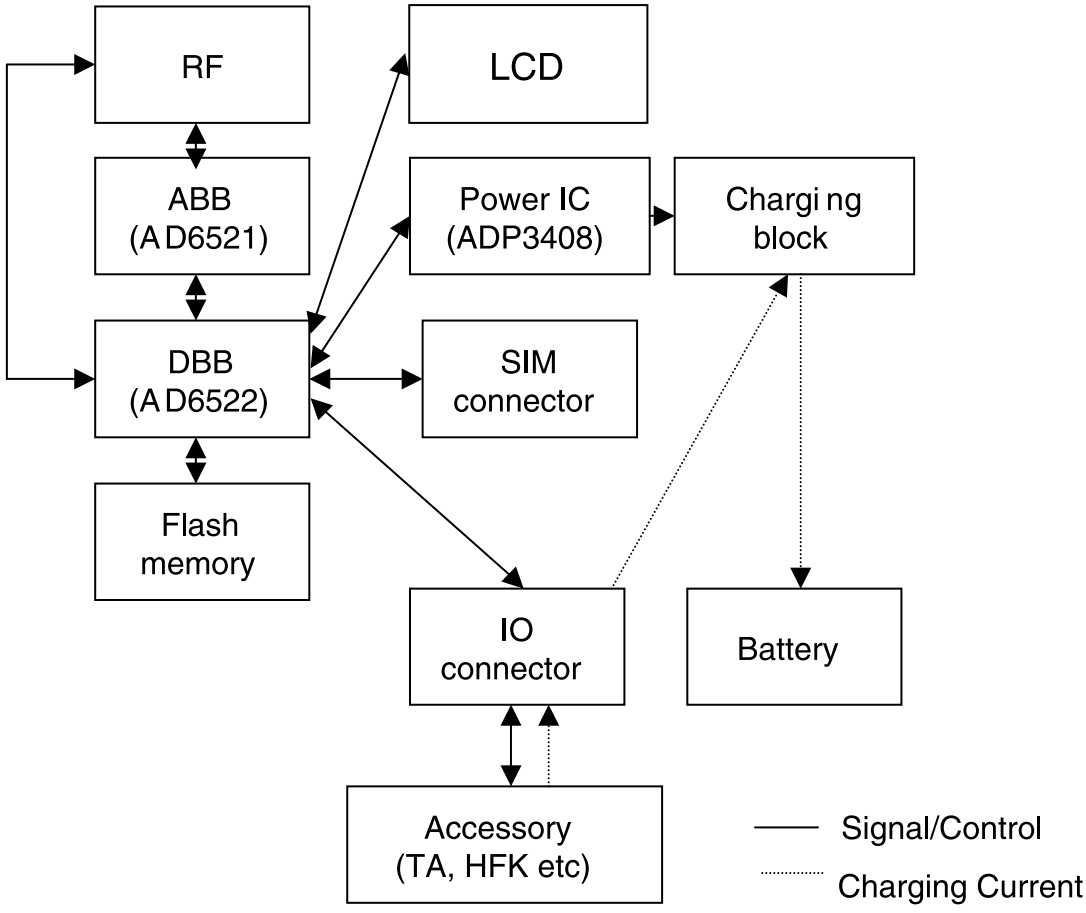
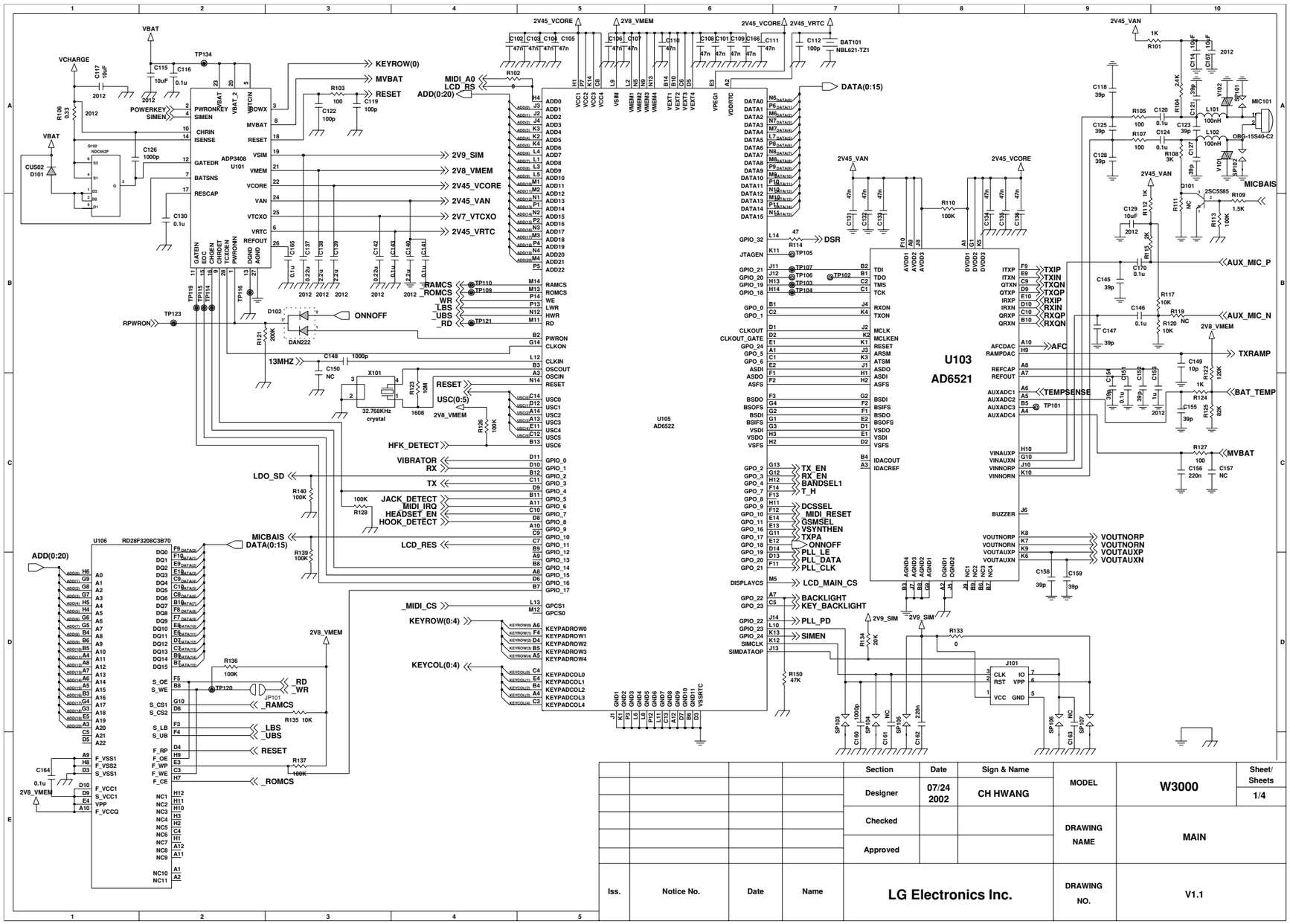


Figure 7-1. block diagram

8. CIRCUIT DIAGRAM

8.1 MAIN



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				Checked			DRAWING NAME	MAIN	
				Approved					
Iss.	Notice No.	Date	Name	LG Electronics Inc.			DRAWING NO.	V1.1	

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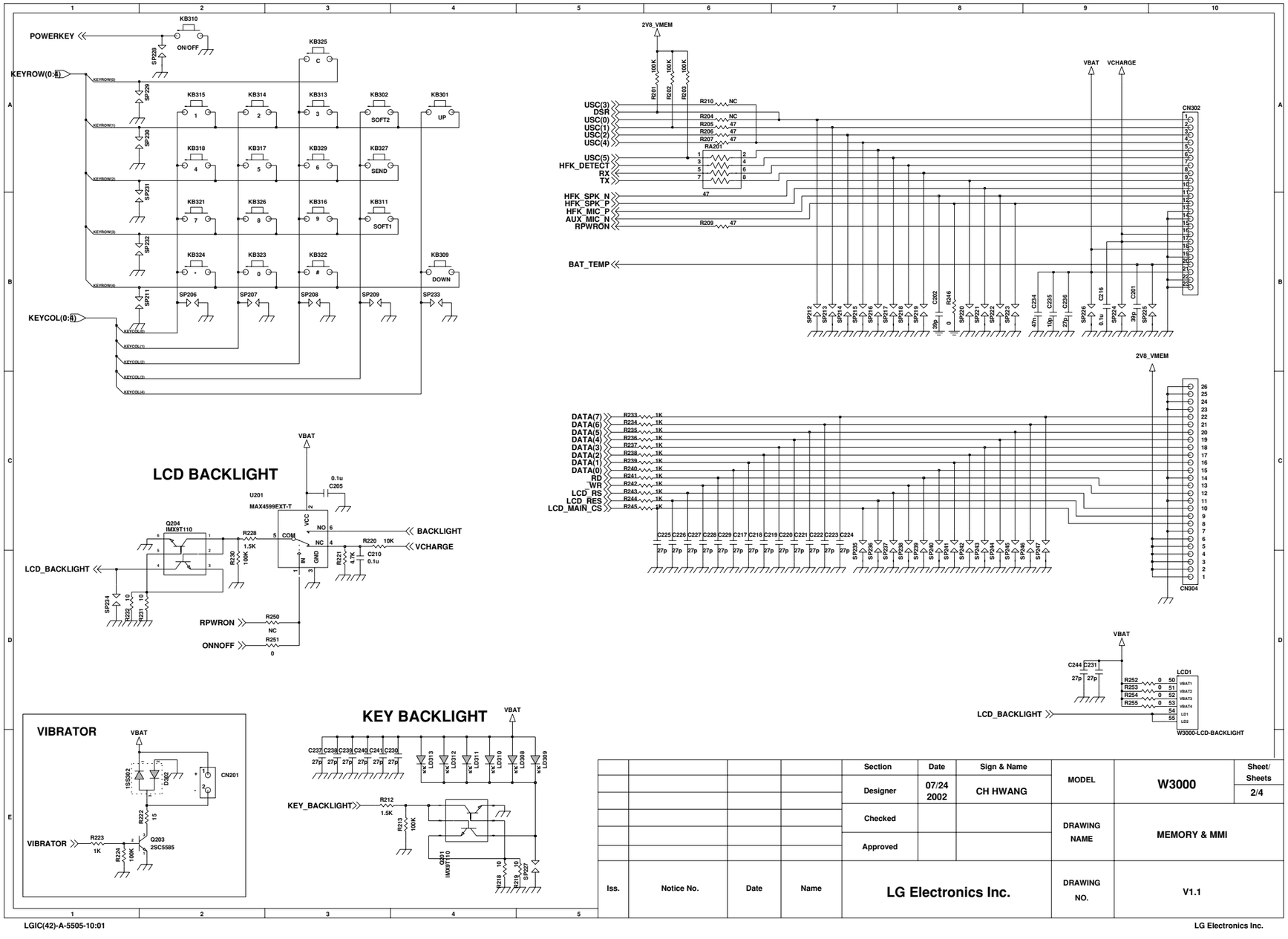
8.2 MEMORY & MMI



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				Checked			DRAWING NAME	MEMORY & MMI	
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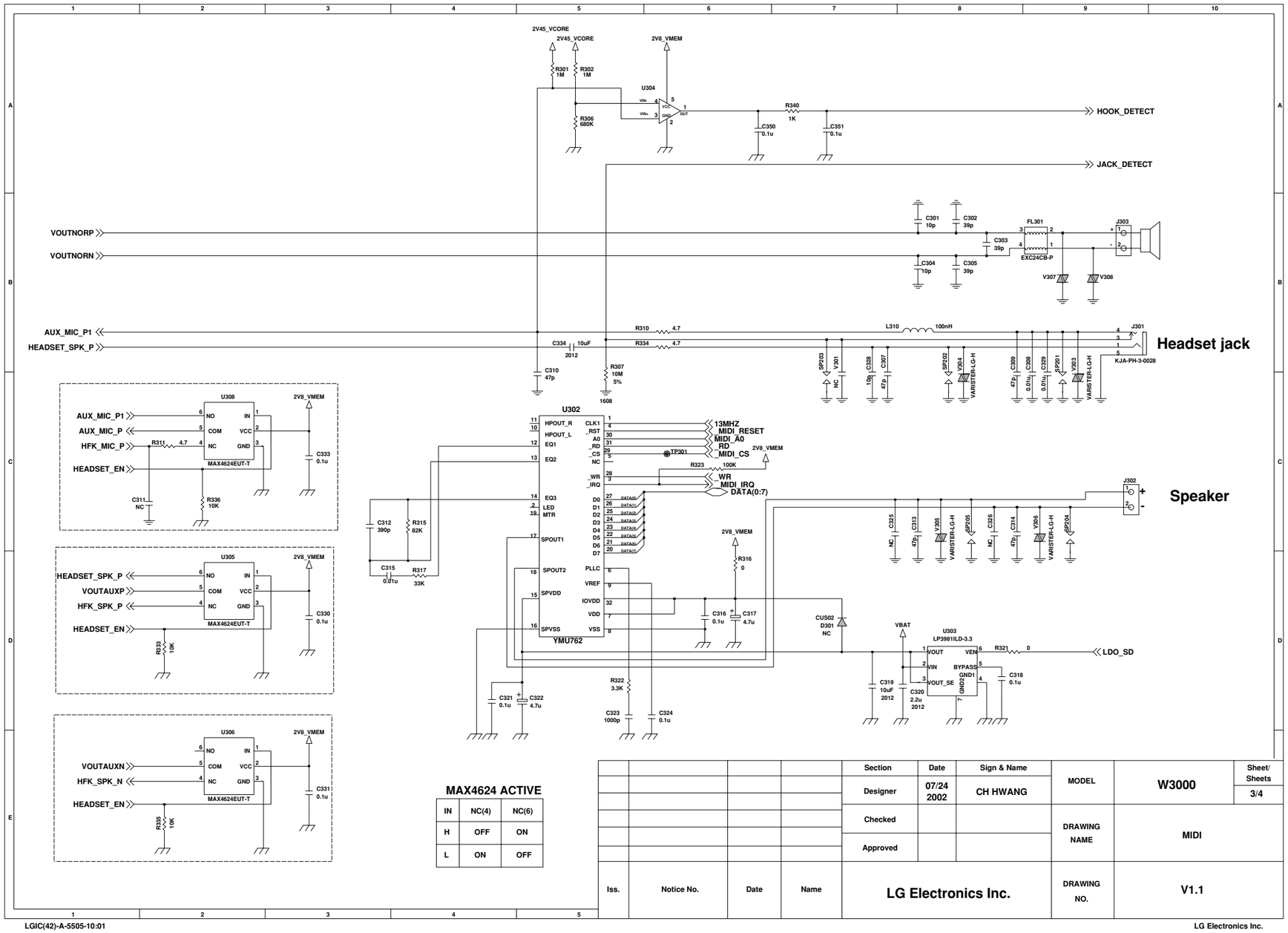
8.3 MIDI


2V45_VCORE
2V8_VMEM
R301 1M
R302 1M
R306 680K
U304
R340 1K
C350 0.1u
C351 0.1u
HOOK_DETECT
JACK_DETECT
VOUTNORP
VOUTNORN
C301 10p
C302 39p
C303 39p
C304 10p
C305 39p
FL301
EXC24CB-P
J303
V307
V308
AUX_MIC_P1
HEADSET_SPK_P
C334 10uF
2012
R310 4.7
R334 4.7
L310 100nH
J301
KJA-PH-3-0028
Headset jack
C310 47p
R307 10M 5%
1608
SP203
V301
NC
C328 10p
C307 47p
SP202
V304
VARISTER-LG-H
C309 47p
C308 0.01u
C329 0.01u
SP201
V303
U308
AUX_MIC_P1
AUX_MIC_P
HFK_MIC_P
HEADSET_EN
R311 4.7
MAX4624EUT-T
C333 0.1u
C311 NC
R336 10K
U305
HEADSET_SPK_P
VOUTAUXP
HFK_SPK_P
HEADSET_EN
R333 10K
C330 0.1u
U306
VOUTAUXN
HFK_SPK_N
HEADSET_EN
R335 10K
C331 0.1u
U302
HPOUT_R
HPOUT_L
EQ1
EQ2
EQ3
LED
MTR
SPOUT1
SPOUT2
SPVDD
SPVSS
CLK1
_RST
A0
_RD
_CS
NC
_WR
_IRQ
D0
D1
D2
D3
D4
D5
D6
D7
PLLC
VREF
IOVDD
VDD
VSS
YMU762
13MHZ
MIDI_RESET
MIDI_A0
_RD
_MIDI_CS
TP301
R323 100K
_WR
MIDI_IRQ
DATA(0:7)
C312 390p
R315 82K
C315 0.01u
R317 33K
R316 0
C316 0.1u
C317 4.7u
C321 0.1u
C322 4.7u
R322 3.3K
C323 1000p
C324 0.1u
J302
Speaker
C325 NC
C313 47p
V305
SP205
C326 NC
C314 47p
V306
SP204
CUS02
D301
NC
VBAT
U303
LP3981ILD-3.3
VOUT
VIN
VOUT_SE
VEN
BYPASS
GND1
GND2
R321 0
LDO_SD
C319 10uF 2012
C320 2.2u 2012
C318 0.1u
MAX4624 ACTIVE
IN | NC(4) | NC(6)
H | OFF | ON
L | ON | OFF
Section | Date | Sign & Name | MODEL | W3000 | Sheet/ Sheets
Designer | 07/24 2002 | CH HWANG | | | 3/4
Checked | | | DRAWING NAME | MIDI
Approved | | | | |
Iss. | Notice No. | Date | Name | LG Electronics Inc. | DRAWING NO. | V1.1
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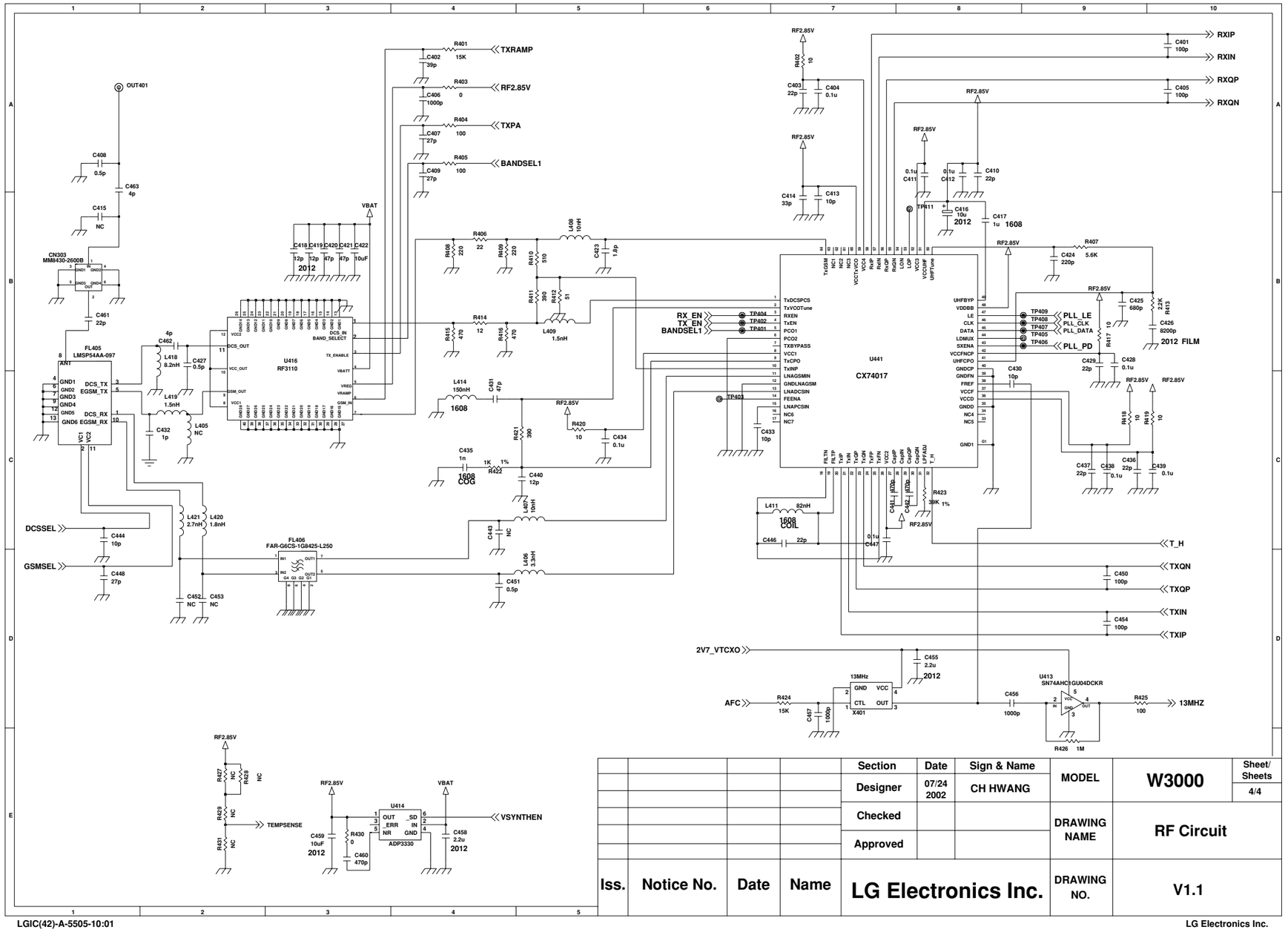
8.4 RF CIRCUIT



				Section	Date	Sign & Name	MODEL	W3000	Sheet/ Sheets
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				Checked			DRAWING NAME	RF Circuit	
				Approved					
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