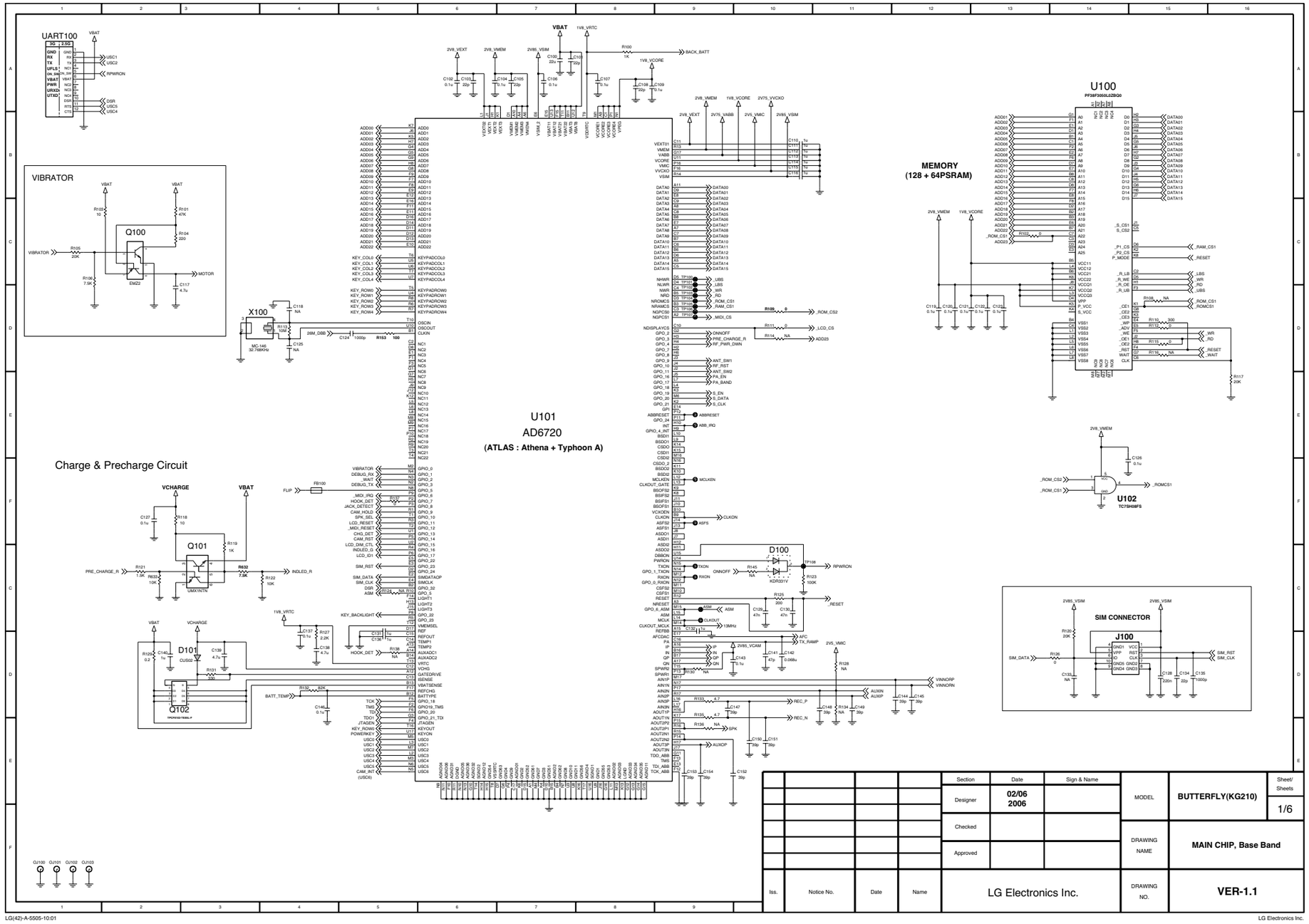
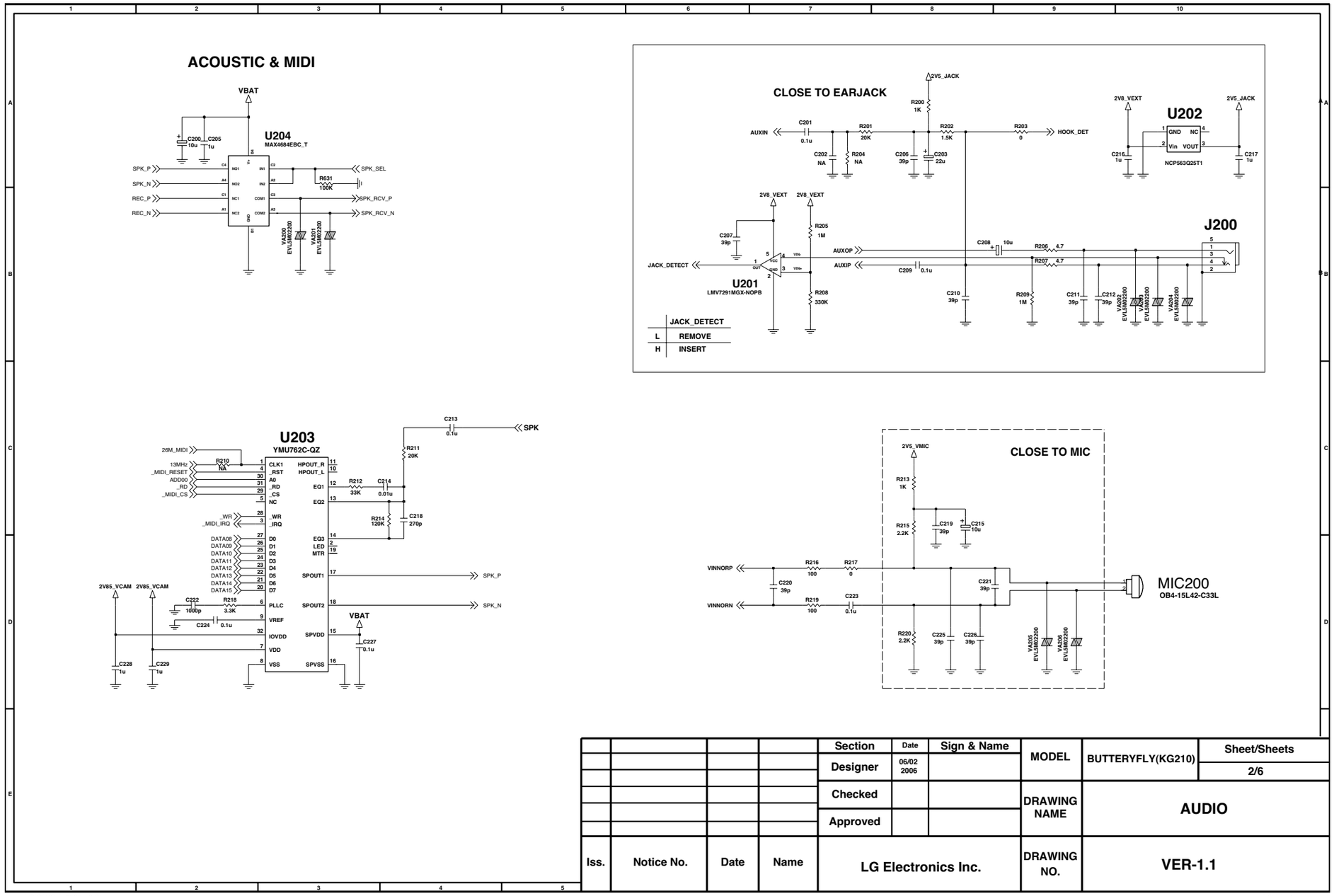
7. CIRCUIT DIAGRAM



7. CIRCUIT DIAGRAM

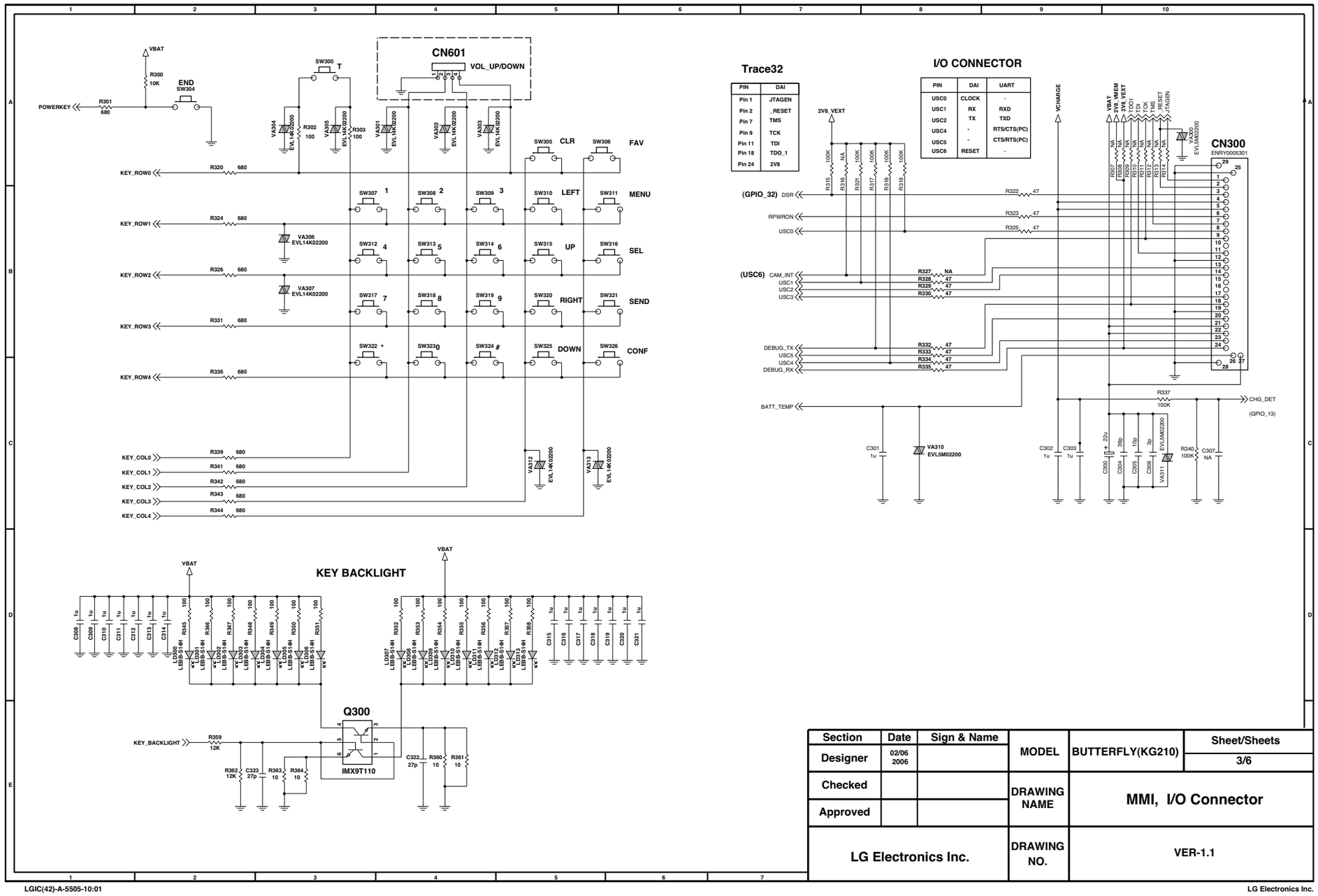


				Section	Date	Sign & Name	MODEL	BUTTERYFLY(KG210)	Sheet/Sheets
				Designer	06/02 2006				2/6
				Checked			DRAWING NAME	AUDIO	
				Approved					
Iss.	Notice No.	Date	Name	LG Electronics Inc.			DRAWING NO.	VER-1.1	

LGIC(42)-A-5505-10:01

LG Electronics Inc.

7. CIRCUIT DIAGRAM

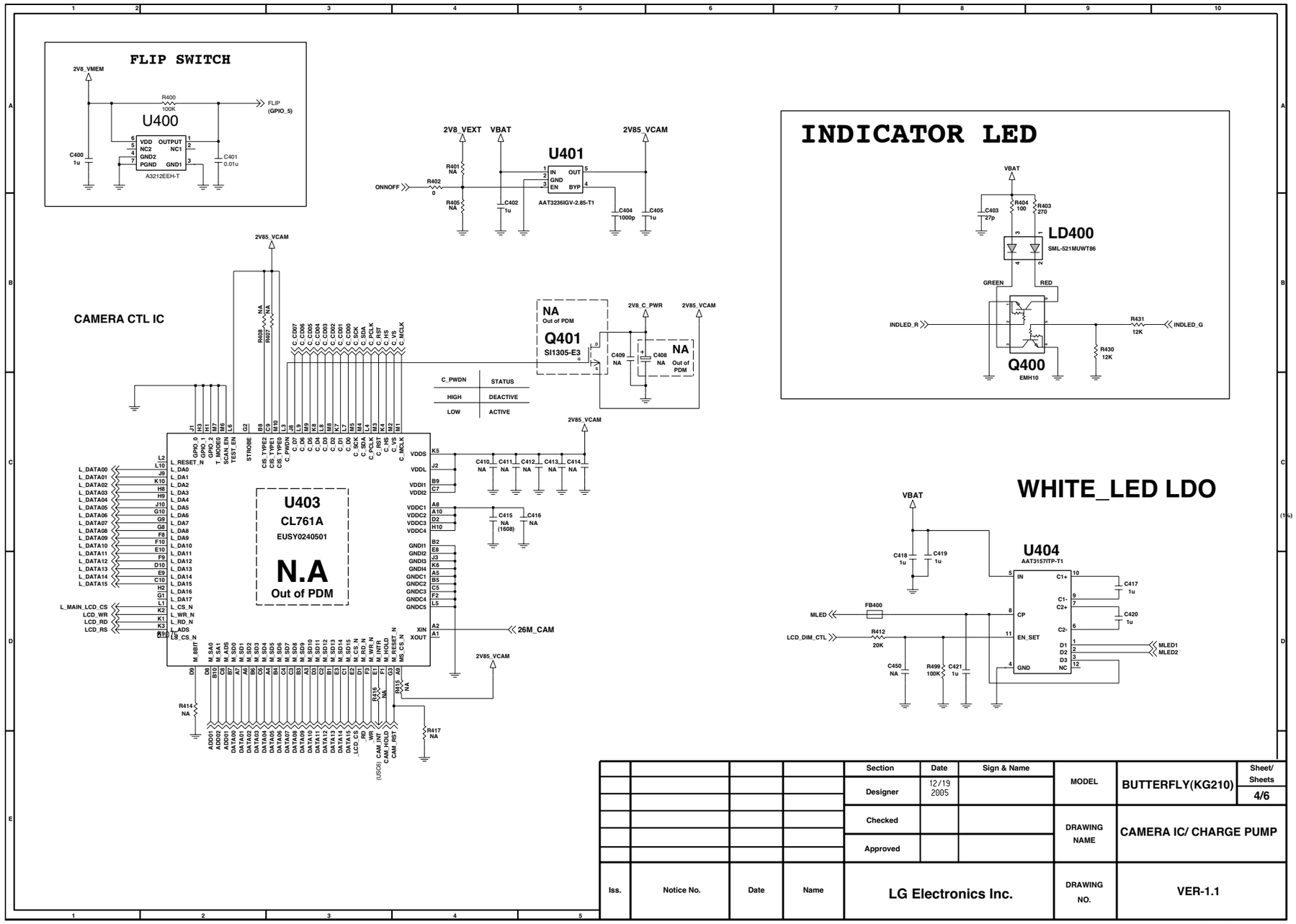


Section	Date	Sign & Name	MODEL	BUTTERFLY(KG210)	Sheet/Sheets
Designer	02/06 2006				3/6
Checked			DRAWING NAME	MMI, I/O Connector	
Approved					
LG Electronics Inc.			DRAWING NO.	VER-1.1	

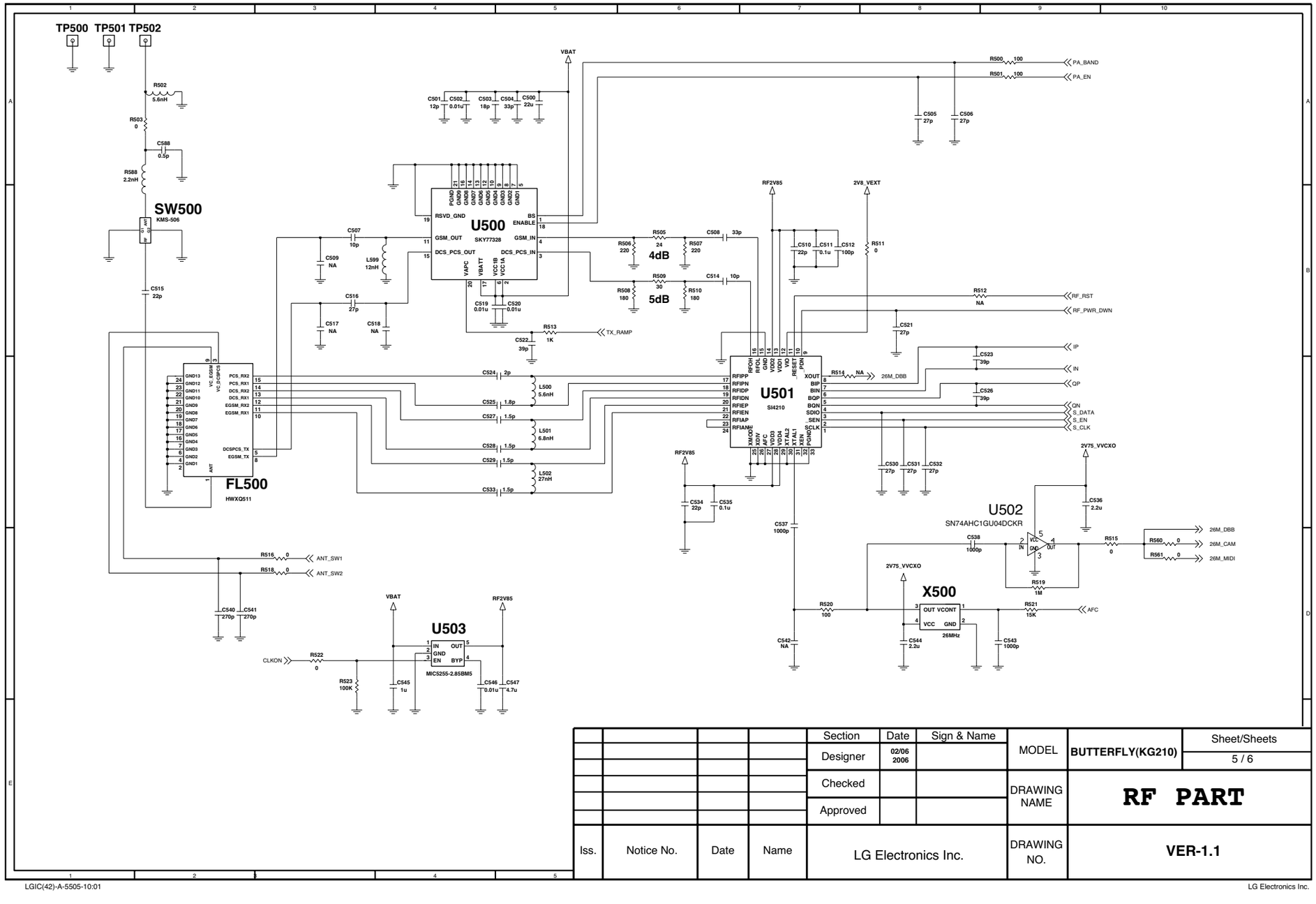
LGIC(42)-A-5505-10:01

LG Electronics Inc.

7. CIRCUIT DIAGRAM



7. CIRCUIT DIAGRAM

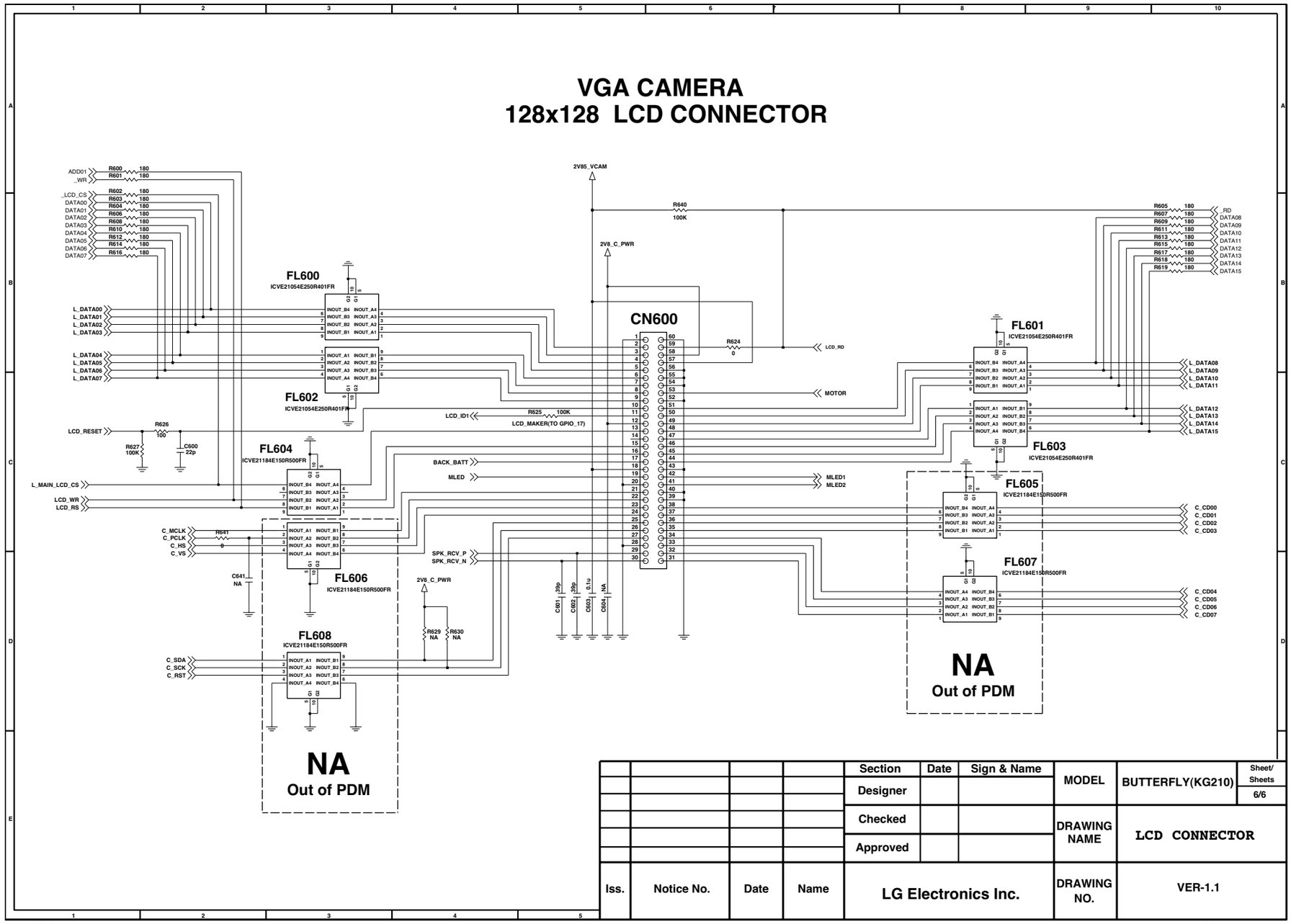


				Section	Date	Sign & Name	MODEL	BUTTERFLY(KG210)	Sheet/Sheets
				Designer	02/06 2006				5 / 6
				Checked			DRAWING NAME	RF PART	
				Approved					
Iss.	Notice No.	Date	Name	LG Electronics Inc.			DRAWING NO.	VER-1.1	

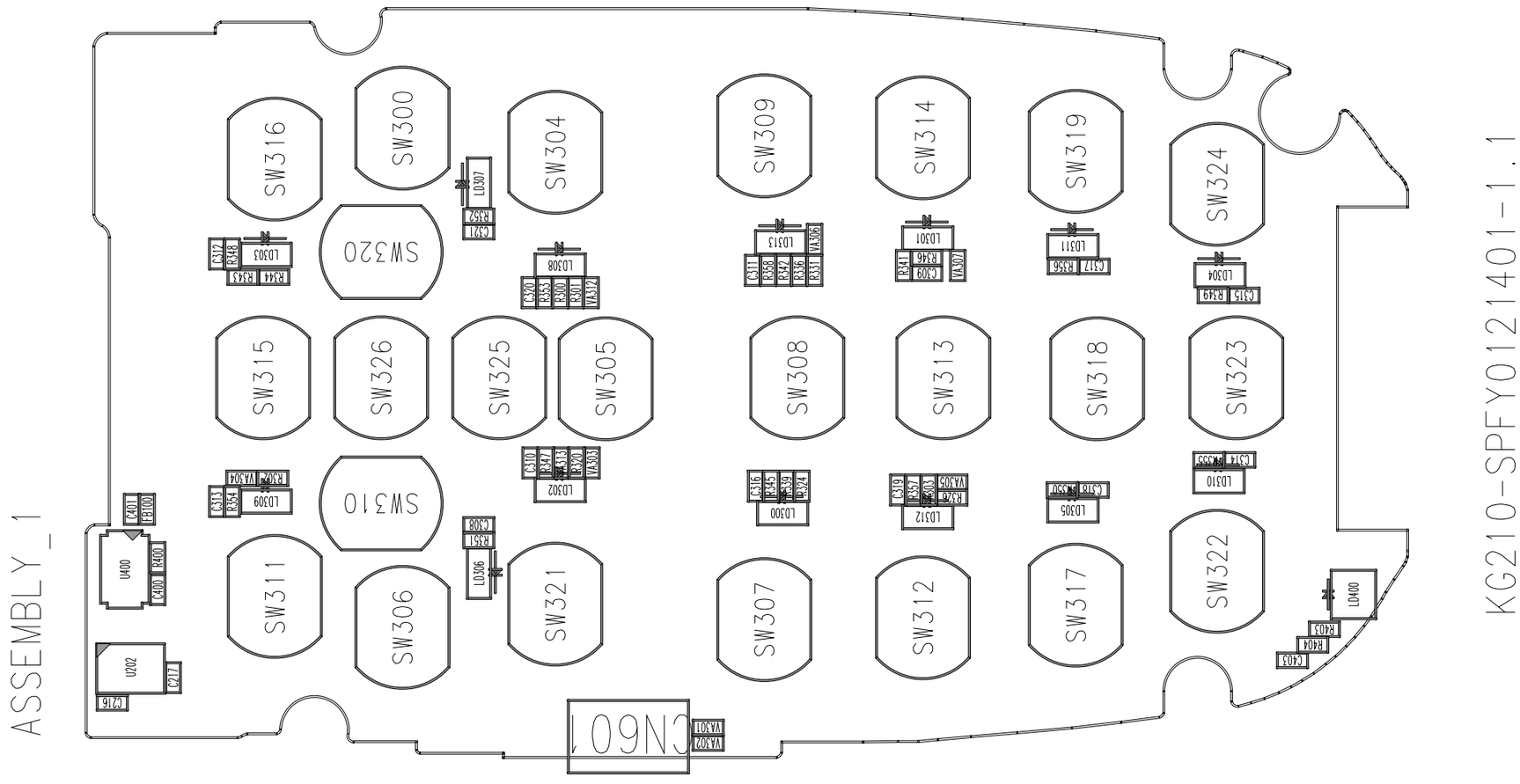
LGIC(42)-A-5505-10:01

LG Electronics Inc.

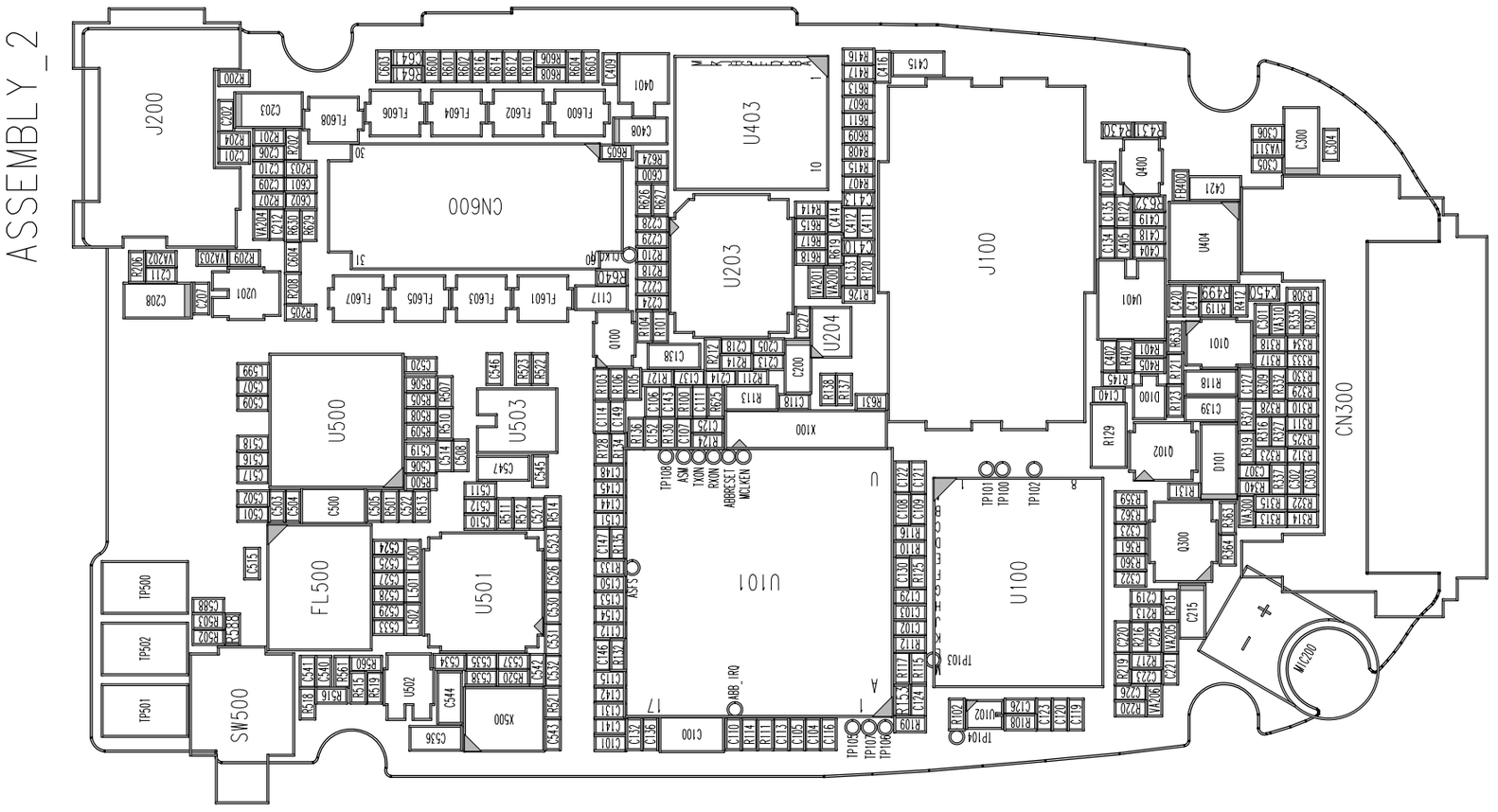
7. CIRCUIT DIAGRAM



8. PCB LAYOUT


ASSEMBLY_1
KG210-SPFY012140-1.1
SW316
SW300
SW304
SW309
SW314
SW319
SW324
SW320
SW315
SW326
SW325
SW305
SW308
SW313
SW318
SW323
SW310
SW311
SW306
SW321
SW307
SW312
SW317
SW322
CN601
U400
U202
LD307
LD308
LD313
LD301
LD311
LD304
LD303
LD309
LD302
LD300
LD312
LD305
LD310
LD306
LD400

8. PCB LAYOUT


ASSEMBLY_2
KG210-SPFY0121401-1.1
J200
CN600
U403
U203
J100
U204
U500
U503
FL500
U501
U101
U100
CN300
SW500
TP500
TP502
TP501
X500
X100
Q300
Q400
Q101
Q102
D101
D100
U401
U404
U201
U502
U102
M1C200