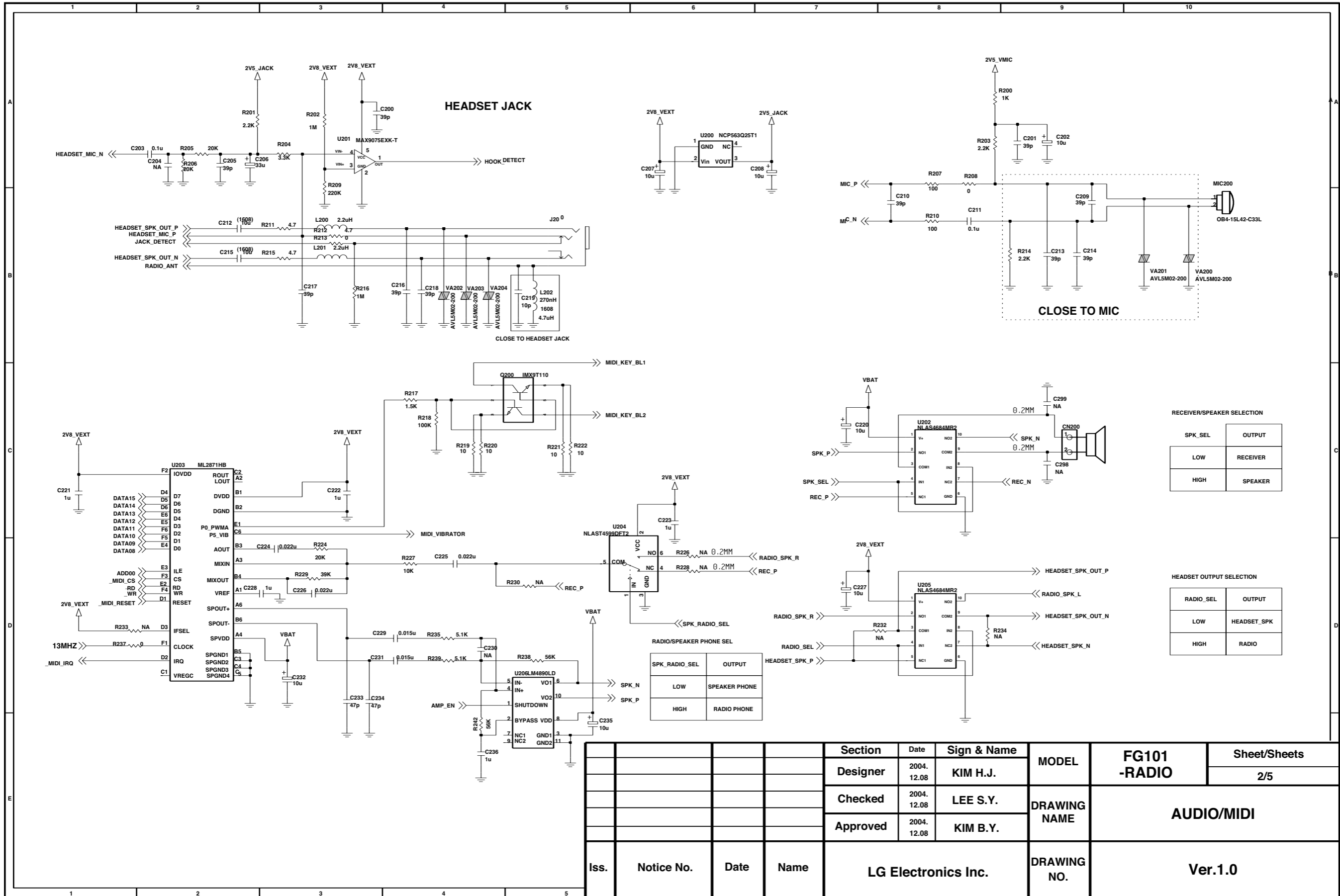


7. CIRCUIT DIAGRAM



RECEIVER/SPEAKER SELECTION

SPK_SEL	OUTPUT
LOW	RECEIVER
HIGH	SPEAKER

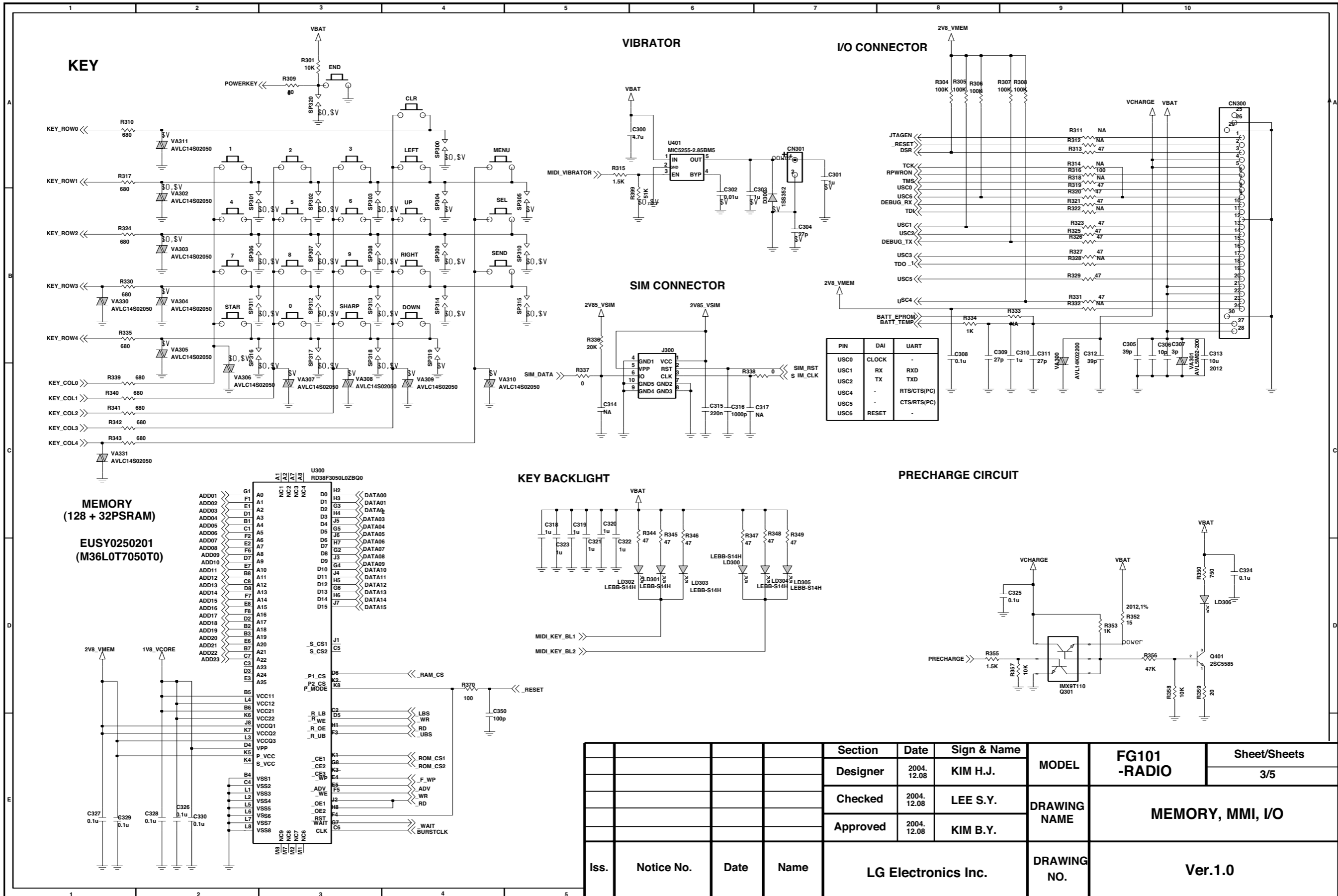
HEADSET OUTPUT SELECTION

RADIO_SEL	OUTPUT
LOW	HEADSET_SPK
HIGH	RADIO

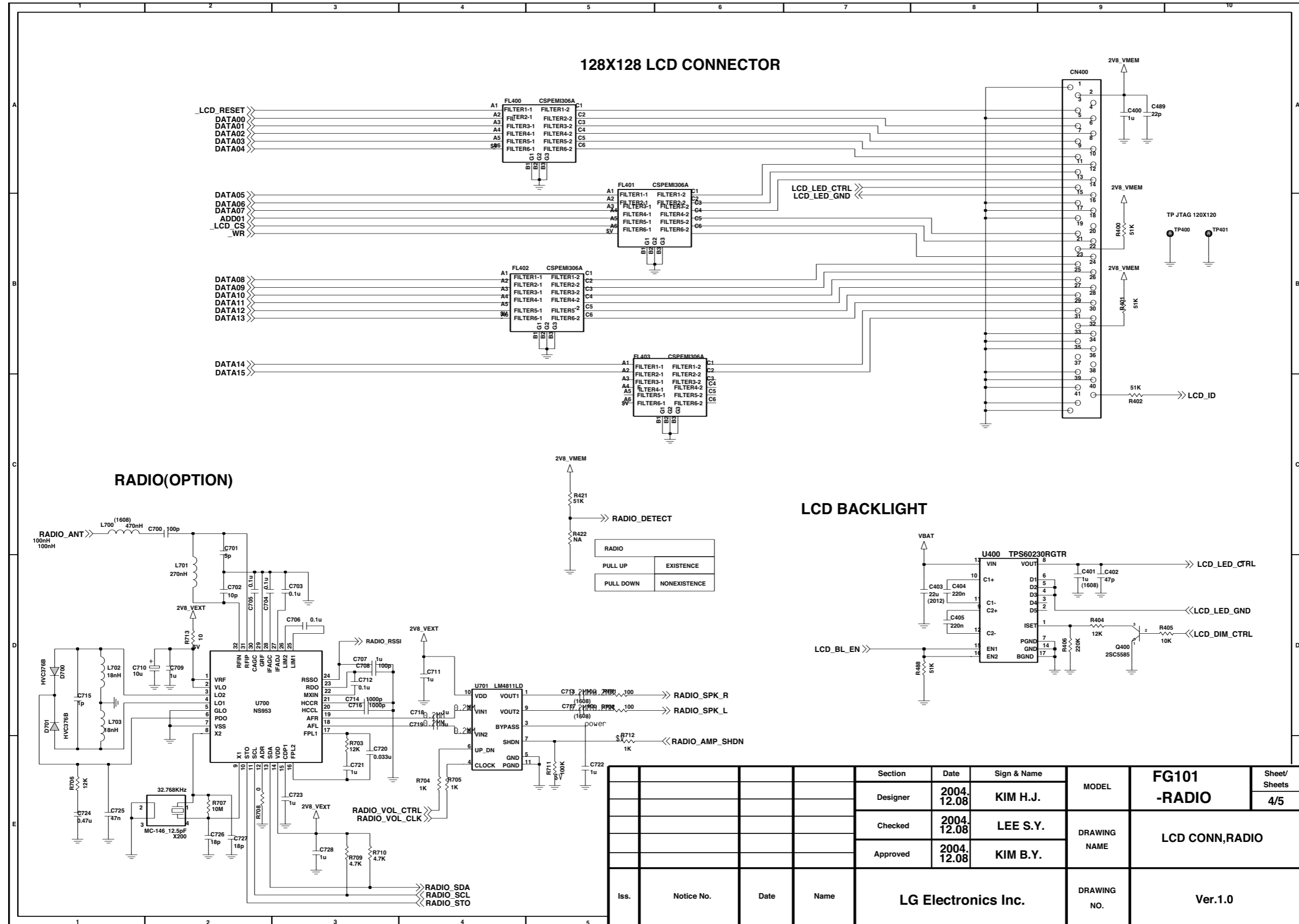
RADIO/SPEAKER PHONE SEL

SPK_RADIO_SEL	OUTPUT
LOW	SPEAKER PHONE
HIGH	RADIO PHONE

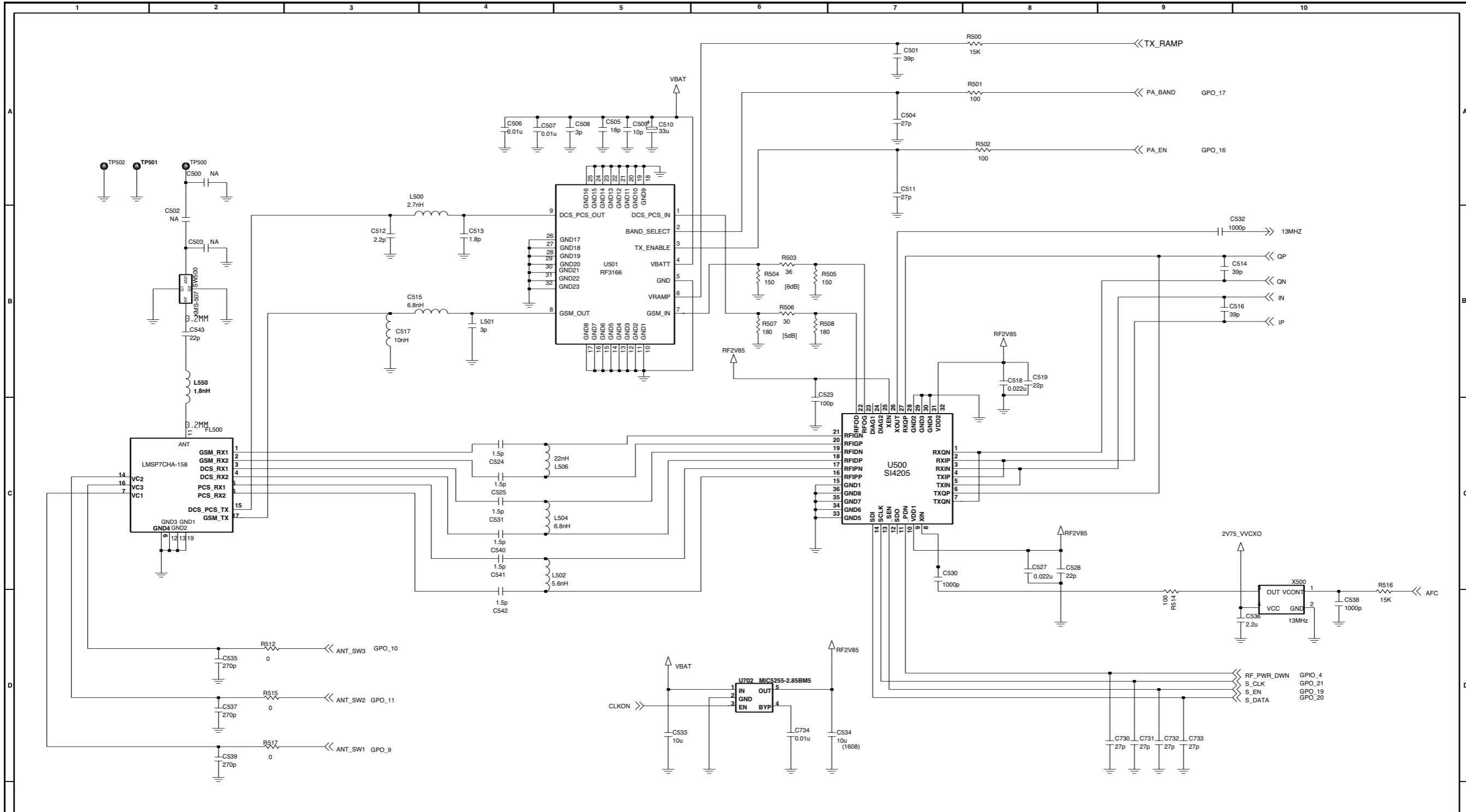
Section	Date	Sign & Name	MODEL	FG101 -RADIO	Sheet/Sheets	
Designer	2004.12.08	KIM H.J.				DRAWING NAME
Checked	2004.12.08	LEE S.Y.			DRAWING NO.	
Approved	2004.12.08	KIM B.Y.				
Iss.	Notice No.	Date	Name	LG Electronics Inc.		



7. CIRCUIT DIAGRAM



Section	Date	Sign & Name	MODEL	FG101 -RADIO	Sheet/ Sheets
Designer	2004.12.08	KIM H.J.			4/5
Checked	2004.12.08	LEE S.Y.			
Approved	2004.12.08	KIM B.Y.			
LG Electronics Inc.			DRAWING NO.	LCD CONN,RADIO	
Iss.	Notice No.	Date	Name	DRAWING NO.	Ver.1.0



ANT SW CONTROL TABLE

	VC1	VC2	VC3
EGSM_TX	L	L	H
DCS/PCS_TX	L	H	L
EGSM/DCS_RX	L	L	L
PCS_Rx	H	L	L

Section	Date	Sign & Name	MODEL	FG101 -RADIO	Sheet/Sheets
Designer	2004.12.08	KIM Y.G.			5/5
Checked	2004.12.08	LEE S.Y.	DRAWING NAME	RF	
Approved	2004.12.08	KIM B.Y.			
Iss.	Notice No.	Date	Name	DRAWING NO.	Ver.1.0
LG Electronics Inc.					

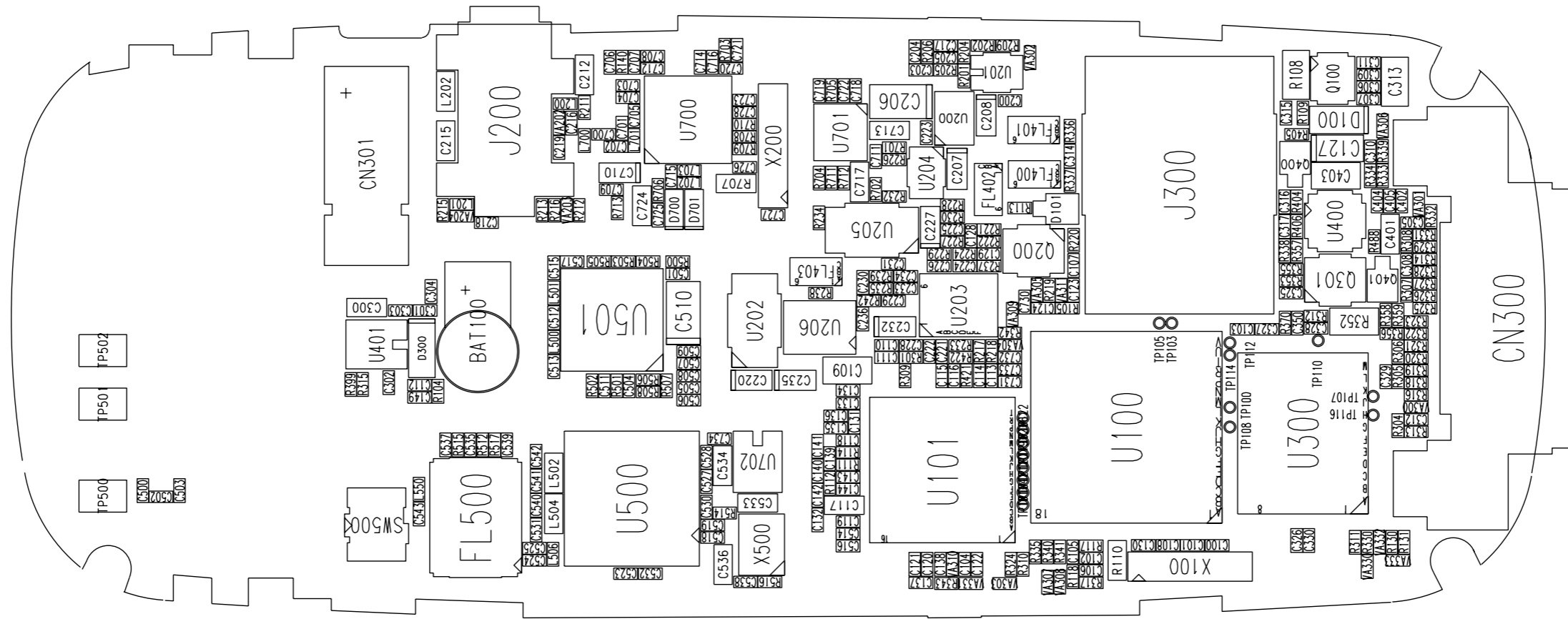


Figure 8-1 B2000 BOTTOM SIDE PCB LAYOUT

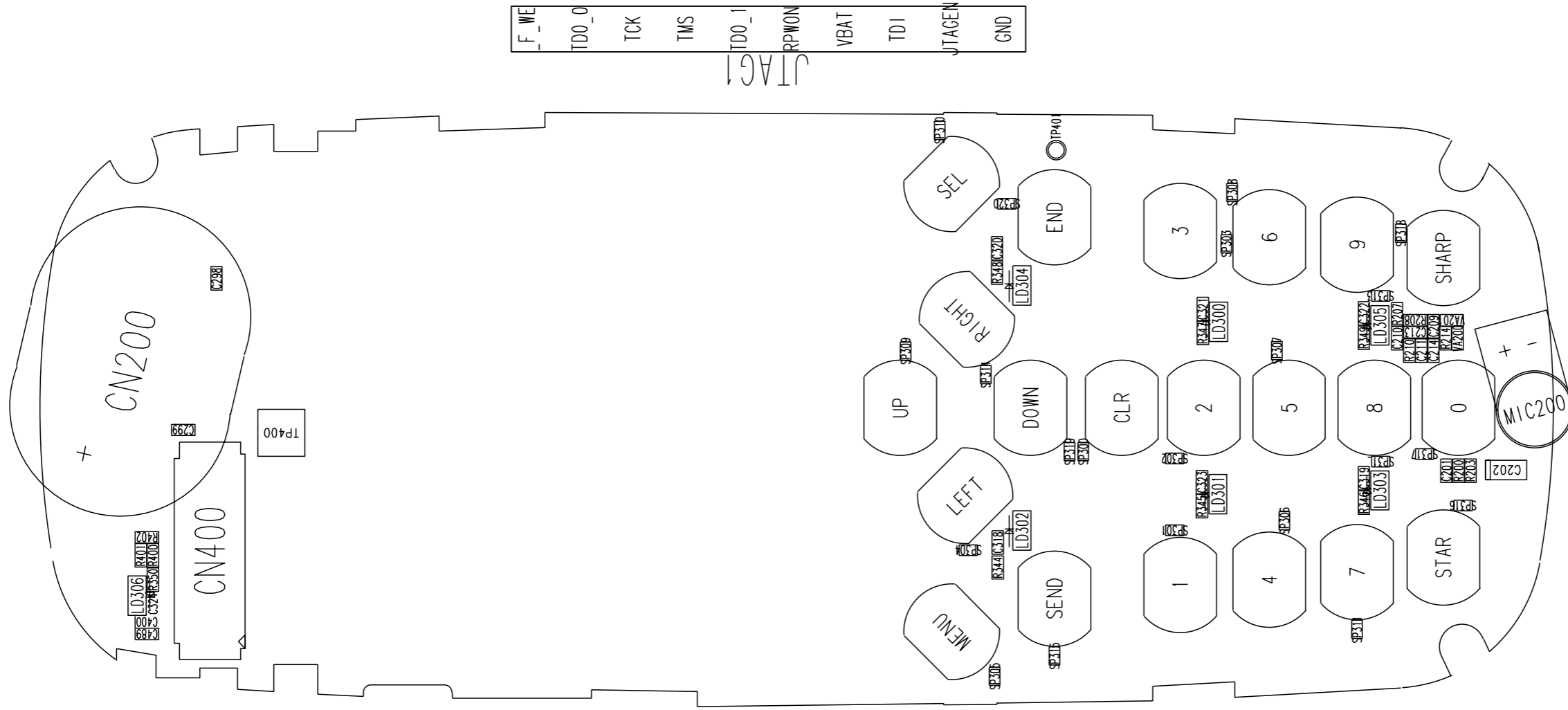


Figure 8-1 B2000 TOP SIDE PCB LAYOUT